

<b>Notice of References Cited</b>		Application/Control No.	Applicant(s)/Patent Under Reexamination GILMAN ET AL.	
		Examiner Carl Colin	Art Unit 2136	Page 1 of 1

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**NON-PATENT DOCUMENTS**

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*	V	BLOSS et al, "E-Manufacturing Opportunities in Semiconductor Processing" Semiconductor International, pp. 88-96 (July 2001)
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.